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Amendments to the Claims:

Claims 1-35 (Cancelled)

36. (Original) A circuit, comprising a filter circuit coupled to receive a sequence of predetermined signals from a first and a second remote antenna, wherein the sequence of predetermined signals comprises a code sequence, and wherein a first shift of the code sequence corresponds to the first remote antenna and a second shift of the code sequence corresponds to the second remote antenna, the filter circuit producing an output signal in response to the data signals.

37. (Original) A circuit as in claim 36, wherein the predetermined signals comprise a midamble.

38. (Original) A circuit as in claim 36, wherein said each respective shift of a code of the predetermined signals is a respective shifted sample of a code sequence.

39. (Canceled)

40. (New) A circuit as in claim 36, wherein said each respective shift of a code of the predetermined signals is a different circular shift of a code sequence.

41. (New) A method of communicating, comprising the steps of:  
producing a first code sequence;  
producing a second code sequence by shifting the first code sequence;  
transmitting the first code sequence from a first transmit antenna; and  
transmitting the second code sequence from a second transmit antenna.

TI-28984A, Page 7

42. (New) A method as in claim 41, wherein each of the first and second code sequences comprises a respective midamble.
43. (New) A method as in claim 41, wherein the step of producing a second code sequence by shifting the first code sequence comprises circularly shifting the first code sequence.
44. (New) A method as in claim 41, comprising the steps of:  
copying a first number of bits from the first code sequence;  
appending the first number of bits from the first code sequence to the first code sequence;  
copying the first number of bits from the second code sequence; and  
appending the first number of bits from the second code sequence to the second code sequence.
45. (New) A method as in claim 44, wherein the step of copying a first number of bits produces a first cyclic prefix, and wherein the step of copying a second number of bits produces a second cyclic prefix.